

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/512,511	FADAVI-ARDEKANI ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Daniel Pan	2183	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the paper filed on 06/28/04.
2. ☒ The allowed claim(s) is/are 1,2,4-8 (claim 3 has been canceled).
3. ☒ The drawings filed on 30 May 2000 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All   b) ☐ Some\*   c) ☐ None   of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____  |

DANIEL H. PAN  
PRIMARY EXAMINER  
09/04/04

### Reasons for Allowance

None of the prior art of record teaches the combined features of the hardware accelerator, the parameter RAM coupled to the hardware accelerator and adapted to store exclusively data for populating variables of programming instructions for use in programming instructions stored elsewhere and used by the hardware accelerator, the DSP in connection with the plural ADSL lines, and the RAM selectively configurable to store operating condition parameters for each of the plural ADSL lines (newly amended claim 1).

### Prior art of Record

Kaplan (6,704,871) was cited (03/24/04) for the teaching of the parameter RAM for storing the data set [keyset] (see fig.1, [32]). However, it failed to teach the plurality of ADSL lines, nor the combined features of the hardware accelerator, the parameter RAM coupled to the hardware accelerator and adapted to store exclusively data for populating variables of programming instructions for use in programming instructions stored elsewhere and used by the hardware accelerator, the DSP in connection with the plural ADSL lines, and the RAM selectively configurable to store operating condition parameters for each of the plural ADSL lines as claimed.

Maki (4,839,851) was cited (Office Action ,02/05/03) for the teaching of the RAM connected with hardware accelerator (e.g. see col.3, lines 4-8, col.1, lines 4-8).

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However, the RAM was used for storing the program and data, (see the T and P states stored in memory in col.1, lines 10-52), and it failed to teach the RAM was used to store exclusively the data for populating variables of programming instructions, nor the combined features of the hardware accelerator, the parameter RAM coupled to the hardware accelerator and adapted to store exclusively data for populating variables of programming instructions for use in programming instructions stored elsewhere and used by the hardware accelerator, the DSP in connection with the plural ADSL lines, and the RAM selectively configurable to store operating condition parameters for each of the plural ADSL lines.

Hoekstra (5,883,907) was used for supplementing the teaching of the plural ADSL lines. However, it did not teach the combined features of the hardware accelerator, the parameter RAM for the storage of the exclusively data for populating variables of programming instructions used by the hardware accelerator, the DSP in connection with the plural ADSL lines, and selectively configurable RAM to store operating condition parameters for each of the plural ADSL lines.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dan Pan whose telephone number is 703 305 9696. The examiner can normally be reached on M-F from 8:30 AM to 4:00 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chan, can be reached on 703 305 9712. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

*21 Century Strategic Plan*

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